Uniformly reweighted APP Decoder for memory efficient decoding of LDPC Codes

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Abstract—In this paper we propose a uniformly reweighted a posteriori probability (APP) decoder. It is derived as an algorithm of approximate Bayesian inference on the LDPC code graph, and a correction parameter is introduced and numerically optimized to overcome the suboptimality of the decoder and improve performance compared to the original APP decoder. In addition, we propose a memory efficient implementation of the algorithm that requires memory that is linear only in the codeword length.

I. INTRODUCTION

Belief propagation (BP) is an iterative message-passing algorithm for decoding low density parity check (LDPC) codes, widely used in many systems. Despite its good error correction performances and capability of approaching the Shannon limit, BP suffers from large memory requirements for message processing and storage, proportional to the number of edges in the Tanner graph of the code. Such large memory requirements, coupled with additional hardware resources needed for the message updating, make the BP less attractive in applications with stringing constraints.

A posteriori probability (APP) decoder [1] is a suboptimal alternative to BP, in which the variable node processing is simplified by allowing variables to send messages in an intrinsic manner, and a message from a variable node corresponds to a posteriori value used to estimate that variable. This property admits a memory efficient implementation.

The APP decoder can be seen as an iterative Bayesian procedure which computes the a posteriori bit probabilities conditioned on the previously estimated probabilities and the set of check node constraints. Its suboptimality comes from the approximation by which only the closest check constraint neighborhood of a variable node is taken into account during the iterative Bayesian procedure.

In this paper we propose a parameterized version of the APP decoder called uniformly reweighted APP decoder (URAPP), which can be seen as a suboptimal variant of the tree reweighted belief propagation [2], [3]. The algorithm is derived in the Bayesian framework and the source of the suboptimality is recognized and treated using a reweighted parameter which, in general, depends on the iteration and node considered. As the simplest case, we consider the uniformly reweighted APP decoder, where a constant value is used for the correction parameter and optimized through simulations. The experiments on binary symmetric channel show that the optimal parameter value does not depend on the cross-over probability value and that URAPP has significantly better performance than the original version of the APP introduced in [1].

Although the APP decoders can be implemented in a memory efficient way, this advantage has not been recognized in the original paper [1]. On the other hand, the existing memory-aware hardware architectures of APP decoders [4], [5] suffer from the performance degradation caused by further approximations of variable node processing function, introduced to reduce complexity. As the second contribution of the paper, we propose a memory-efficient realization of the reweighted APP decoder, which requires memory proportional to the number of nodes in the Tanner graph of the LDPC code, rather than to the number of edges, as proposed in [1], which is a significant saving.

The paper is organized as follows. In Section II we introduce the notation and give definitions. The URAPP decoder is derived in the Section III, while its memory efficient implementation is considered in the Section IV. The simulation results are presented in the Section V.

II. PRELIMINARIES

An regular LDPC code is a linear block code defined by a sparse parity-check matrix $H$. We denote by $(M, N)$ the size of $H$. A codeword is a vector $\mathbf{x} = (x_1, x_2, \ldots, x_N) \in \{0, 1\}^N$ that satisfies $H\mathbf{x}^T = 0$, where $\mathbf{x}^T$ denotes the transposed (column) vector. The Tanner graph [6] of an LDPC code is a bipartite graph whose adjacency matrix is the parity-check matrix of the code $H$. It contains two types of nodes: a set of variable-nodes $\mathcal{V} = \{v_1, v_2, \ldots, v_N\}$, corresponding to the $N$ columns of $H$, and a set of check-nodes $\mathcal{C} = \{c_1, c_2, \ldots, c_M\}$, corresponding to the $M$ rows of $H$. A variable-node $v_n$ and a check-node $c_m$ are connected by an edge if and only if the corresponding entry of $H$ is non-zero.

The set of indices of check-nodes connected to the variable-node $v_n$ is denoted with $\mathcal{M}(n)$ and the set of indices of variable-nodes connected to the check-node $c_m$ is denoted with $\mathcal{N}(m)$. The direct neighborhood of a node $v_n$ is defined with $\mathcal{M}(n) = \bigcup_{m \in \mathcal{M}(n)} \mathcal{N}(m) \cap v_n$.

We consider the binary symmetric channel (BSC), where a binary codeword $\mathbf{x}$ is transmitted, and we denote by $y =$
The goal of the decoding is to compute the a posteriori probability

\[ \Pr(x_n | y, C) \]  

where \( C = \{ \sum_{k \in N(m)} x_k = 0 \}_{m \in M} \) is the set of constraints defined by the parity check matrix. According to the channel model the a posteriori probability can be expressed by the usage of the Bayesian formula

\[ \Pr(x_n | y, C) \propto \Pr(y_n | x_n) \times \Pr(C | x_n, y_n), \]  

where we denote \( y_n = (y_1, \ldots, y_{n-1}, y_{n+1}, \ldots, y_N) \).

Belief propagation (BP) has been introduced to approximately compute the a posteriori probability (1) by iterative computation of the check likelihoods \( \Pr(C | x_n, y_n) \). Using the computed check likelihoods, BP in each iteration successively produces estimated a posteriori probabilities \( \{ \hat{p}_n \}_{n=1}^N \) for the codeword symbols \( x_n \), which are used for decision making on bit values.

The computation procedure is exact if the Tanner graph corresponding to the code is a tree, while it achieves very good accuracy for sparse codes. Its computational complexity is low, but it requires a memory storage growing with the number of edges in the graph, which is often seen as a limitation for practical applications. In this section we consider its suboptimal variant called the a posteriori probability (APP) decoder, and propose an uniformly reweighted APP decoder, which do not have this drawback.

Let a set of the constraints which corresponds to the neighborhood of the node \( v_n \) be denoted with \( C_n = \{ \sum_{k \in N(m)} x_k = 0 \}_{m \in M(n)} \). Suppose that we have already given the set of estimated probabilities \( \{ \hat{q}_k \}_{k \in M(n)} \) for the bits in the neighborhood of a node \( v_n \) called a priori probabilities. The APP decoder [1] makes simplification of a posteriori probabilities (1) and computes the probabilities \( \Pr(x_n | y, C_n, \{ \hat{q}_k \}_{k \in M(n)}) \) instead of \( \Pr(x_n | y, C) \). According to the Bayes formula we have

\[ \Pr(x_n | y, C) \approx \Pr(x_n | y, C_n, \{ \hat{q}_k \}_{k \in M(n)}) \propto \Pr(y_n | x_n) \times \Pr(C_n | x_n, y_n, \{ \hat{q}_k \}_{k \in M(n)}). \]  

In the similar way as in the BP, the check node likelihoods are computed in an iterative process. First, according to [7], the check likelihoods can be computed as

\[ \Pr(C_n | x_n, y_n, \{ \hat{q}_k \}_{k \in M(n)}) = \prod_{m \in M(n)} \hat{p}_{mn}(x_n), \]  

where

\[
\hat{p}_{mn}(x_n) = \begin{cases} 
\frac{1}{2} - \frac{1}{2} \prod_{k \in N(m) \setminus n} (1 - 2\hat{q}_k(x_n)); & \text{for } x_n = 1 \\
\frac{1}{2} + \frac{1}{2} \prod_{k \in N(m) \setminus n} (1 - 2\hat{q}_k(x_n)); & \text{for } x_n = 0
\end{cases}
\]  

Once we have computed the a posteriori values \( \{ \hat{p}_n \}_{n=1}^N \) using the expression (3), they are are assign to the a priori probabilities \( \{ \hat{p}_n \}_{n=1}^N \) and new iteration starts. As an initial prior probabilities the channel likelihoods \( \Pr(y_n | x_n) \) are taken. Note that the check node likelihoods depend on \( y_n \) only in the first iteration.

Recall that in APP decoders two types approximations for the check likelihood computation are done:

- Only the local constraints (i.e. in the direct neighborhood) are directly taken into account, and
- The check likelihoods are computed using the a priori probabilities which are obtained through the iterative process.

In the following paragraphs we propose the parameterized version of the APP decoder called uniformly reweighted APP decoder (URAPP). The drawbacks of the APP decoder are handled using a reweighting parameter \( \alpha \) for improving the correctness of a check node likelihood computation. The URAPP is derived from the equations (3) and (4), in a way that the probability \( \hat{p}_{mn}(x_n) \), given by (5), is replaced with its \( \alpha \) escort distribution [8]:

\[ \hat{p}_{mn}(x_n) \leftarrow \hat{p}_{mn}^{(\alpha)}(x_n) = \frac{\hat{p}_{mn}(x_n)^\alpha}{\sum_{x_n} \hat{p}_{mn}(x_n)^\alpha}; \quad \alpha \in [0, 1]. \]

Accordingly, the URAPP decoder runs as follows.

1) For all nodes \( v_n \) initialize a priori values to \( \hat{q}_n(x_n) = \Pr(x_n | y_n); \)
2) For all nodes \( v_n \) compute a posteriori probabilities:

\[ \hat{p}_n(x_n) \propto p_n(x_n) \times \prod_{m \in M(n)} \hat{p}_{mn}(x_n)^\alpha, \]

where \( \hat{p}_n(x_n) \) is computed according to (5);
3) For all nodes \( v_n \) set \( \hat{q}_n \leftarrow \hat{p}_n \) and go to 2 until a convergence criterion has been reached.

In the limit cases, \( \alpha = 0 \) and \( \alpha = 1 \), the escort distribution reduces to the uniform and to the original one, respectively. Accordingly, as the \( \alpha \) becomes smaller, the escort distribution becomes closer to the uniform distribution. Put differently, in the case of small \( \alpha \) the check likelihood will be smoothed and the equation (7) will be determined by the channel likelihood. Oppositely, in the case of \( \alpha = 1 \) the URAPP reduces to APP decoder. The optimal value for \( \alpha \) is between 0 and 1. If properly optimized, the parameter \( \alpha \) can significantly improve the accuracy of the a posteriori probability estimation. In Section V we present the experimental results for the URAPP when the \( \alpha \) is determined by a brute force optimization.
IV. MEMORY EFFICIENT IMPLEMENTATION OF URAPP DECODER

For the implementation purposes, in order to avoid the computation of the normalization coefficient in the equation (7) and to reduce the number of multiplications, the APP decoders are commonly represented in log-ratio domain message passing version [1]. The message passing version can be directly derived from the previously defined steps 1-3, if we set
\[ \hat{\gamma}_n = \log \frac{\hat{p}_n(1)}{\hat{p}_n(0)} \quad \text{and} \quad \mu_{m \rightarrow n} = \log \frac{\hat{p}_{mn}(1)}{\hat{p}_{mn}(0)} \]
as follows.

**Initialization:** Variable-nodes are initialized to a priori values \((\gamma_1, \gamma_2, \ldots, \gamma_n)\), which are in the case of BI-AWGN channel equal to the received sequence \((y_1, y_2, \ldots, y_N)\), prior to the first iteration of the APP decoder:
\[ \hat{\gamma}_n^{(0)} = \gamma_n = \frac{p(x_n = 0|y_n)}{P(x_n = 1|y_n)}. \]

**Iterative processing:**

1) Check-node processing: consists in computing the check-to-variable messages \(\mu_{m \rightarrow n}^{(k)}\), for all check-nodes \(m\) and their neighbor variable-nodes \(v_n\):
\[ \mu_{m \rightarrow n}^{(k)} = \bigoplus_{k \in \mathcal{N}(m) \setminus n} \hat{\gamma}_{k}^{(k-1)}, \]
where \(\bigoplus\) stands for the summation over the set \(\mathcal{N}(m) \setminus n\) induced by the box-sum operation defined as
\[ x \boxplus y = \log \frac{1 + e^x e^y}{e^x + e^y}. \]

2) A posteriori information update: consists in computing the a posteriori messages \(\hat{\gamma}_n^{(k)}\), for all variable-nodes \(v_n\),
\[ \hat{\gamma}_n^{(k)} = \gamma_n + \alpha \sum_{m \in \mathcal{N}(n)} \mu_{m \rightarrow n}^{(k)}, \]
where \(\alpha\) is a parameter.

3) Hard decision: Estimated (binary) values of sent bits, \(\hat{x} = (\hat{x}_1, \hat{x}_2, \ldots, \hat{x}_N)\), according to the rule: \(\hat{x}_n = 1\) if \(x_n^{(k)} > 0\) then \(x_n^{(k)} = 0\), otherwise \(\hat{x}_n = \text{sign}(\hat{\gamma}_n^{(k)})\). The decoder stops when either \(\hat{x}\) is a codeword or a maximum number of decoding iterations is reached.

Check to variable messages requires the computation of all partial sums \(\bigoplus_{k \in \mathcal{N}(m) \setminus n} \hat{\gamma}_{k}^{(k-1)}\), which can efficiently be computed using the inverse operation for \(\boxplus\) called minus-box operator:
\[ x \boxminus y = \log \frac{1 - e^x e^y}{e^x - e^y}. \]
It is easy to check that \(x \boxplus y \boxminus y = x\). Using the \(\boxminus\) operator, the sum
\[ \Psi_m^{(k)} = \bigoplus_{k \in \mathcal{N}(m) \setminus n} \hat{\gamma}_{k}^{(k-1)} \]
can be computed once per iteration and node, and all the messages can be computed for all \(n \in \mathcal{N}(m)\) as
\[ \mu_{m \rightarrow n}^{(k)} = \Psi_m^{(k)} - \hat{\gamma}_n^{(k)}. \]

In a common, parallel message passing implementation of the APP decoder, all the variable nodes take the message at same time, the a posteriori values are computed, which completes one iteration. Although this version provides high throughput, it suffers from the high memory requirements, proportional to the number of edges in the Tanner graph, since an iteration requires the storing of all check to variable messages for one iteration.

In the Algorithm 1 we present semi-parallel, memory efficient implementation of the URAPP decoder, based on the ideas proposed in [9] for the BP algorithm. Instead of the messages, we store only the values \(\Psi_m^{k-1} = \bigoplus_{n \in \mathcal{N}(m)} \hat{\gamma}_n^{k-1}\), which are used for the computation of the posterior values \(\hat{\gamma}_n^{k}\), and at one iteration, all variable nodes can be partially updated during the computations in all variable nodes. Both implementations provide exactly the same output after each iteration and have the same computational requirements. Although the former one has the smaller throughput, since two check nodes might try to access the same variable node to update its a posteriori value, it needs the storing only the values in variable nodes. As a result it have the complexity proportional to the number of nodes, which is a significant saving.

**Algorithm 1:** Semi-parallel URAPP decoder

| Input: \(y = (y_1, \ldots, y_N) \in \mathcal{Y}^N\) \(\Rightarrow\) received word |
| Output: \(\hat{x} = (\hat{x}_1, \ldots, \hat{x}_N) \in \{0, 1\}^N\) \(\Rightarrow\) estimated codeword |

**Initialization:**

for each \(\{v_n\}_{n=1,\ldots,N}\) do \(\gamma_n = \log \frac{Pr(x_n = 0|y_n)}{Pr(x_n = 1|y_n)}\);

for each \(\{v_n\}_{n=1,\ldots,N}\) do \(\hat{\gamma}_n^{0} = \gamma_n\);

Iteration loop: \(k > 0\)

Total check-sum computation
for each \(\{c_m\}_{m=1,\ldots,M}\) do \(\Psi_{m}^{k-1} = \bigoplus_{n \in \mathcal{N}(m)} \hat{\gamma}_n^{k-1}\)

partial a posteriori update
for each \(\{c_m\}_{m=1,\ldots,M}\) do

for each \(v_n \in \mathcal{H}(c_m)\) do

\[ \mu = \Psi_m^{k-1} \boxminus \hat{\gamma}_n^{k-1} \]

\[ \hat{\gamma}_n^{k} = \hat{\gamma}_n^{k} + \alpha \mu \]

a posteriori update
for each \(\{v_n\}_{n=1,\ldots,N}\) do \(\hat{\gamma}_n^{k-1} = \hat{\gamma}_n^{k}\)

hard decision
for each \(\{v_n\}_{n=1,\ldots,N}\) do \(\hat{x}_n = (1 - \text{sign}(\hat{\gamma}_n))/2\)

if \(\hat{x}\) is codeword then exit the iteration loop

End iteration loop
V. REWEIGHTING FACTOR OPTIMIZATION AND SIMULATION RESULTS

In this section we present simulation results for the URAPP. We consider Tanner (3, 5) code [6]. Decoders with small number of iterations and suitable for high-throughput systems [5]. Optimal reweighted parameter is obtained through simulations. The results of the optimization procedure, for the maximal number of iterations are set to 5, 10 and 20, are shown in Figures 1, 2 and 3. They show that the optimum value of reweighted factor does
not depend on the cross-over probability, but only on the maximal number of iterations. As discussed in Section III, the reweighted factor slows down the convergence. This agrees with the simulation results, which show that the value of the factor is inversely proportional to the maximum number of iterations. Fig. 4 shows the bit error rate (BER) performances of standard and uniformly reweighted APP decoders, with reweighting parameters obtained from the optimization procedure, for the maximal number of iterations $K = 5$, $K = 10$ and $K = 20$. For $K = 5$, optimal value of the reweighting factor $\alpha$ is 0.7 and there is not a significant BER improvement over the unweighted APP decoder. As the $K$ becomes larger the effect on $\alpha$ is larger, specially in the error floor region, since the reweighting factor emphasizes puts a higher importance on the channel likelihoods.

VI. Conclusion

In this paper we proposed memory efficient uniformly reweighted APP decoder (URAPP) for decoding of LDPC codes, with the memory complexity proportional to the number of nodes in the Tanner graph of the code. The algorithm can be seen as a suboptimal variant of the tree reweighted belief propagation [2], [3] and operates as a parameterized Bayesian inference algorithm, improving the performances of previously propose APP decoder [1]. Suboptimality of the APP decoder comes from the fact that in each iteration and for each variable node only the neighboring check constraints are taken into account. We introduced the reweighting parameter which deals with this source of suboptimallity and empirically showed the significant performance improvements even in the case when the a constant value is used for the correction parameter. Taking into account the graph structure and dynamics of the URAPP decoder for the parameter estimation seem like a promising direction, which is the part of our future work on the parameter adaptive version of the reweighted APP decoder. The simulation results on binary simetric channel for Tanner $(3,5)$ code [6] show that the reweighting factor does not depend on the cross-over probability, but only on the maximal number of iterations taken in to decoder. Currently, we are exploring its dependence on LDPC code parameters.

REFERENCES

[10]